

IAS-YNU Seminar 先端科学高等研究院

4th Seminar of Research Unit for Extremely Energy-Efficient Processors

超省エネルギープロセッサ研究ユニット 第4回セミナー

Date: 13:00 – 16:10, August 3 (Monday), 2015
13:00 – 16:10, August 6 (Thursday), 2015

日程: 平成27年8月3日(月) 13:00~16:10
平成27年8月6日(木) 13:00~16:10

Place: Electrical and Computer Eng. Bldg. 4F, (Seminar room I).
場所: 電子情報工学科棟 (演習室I)

Special lecture



Prof. Thomas Ortlepp,
Distinguished YNU Professor, Yokohama National University
Deputy head of the institute, CiS Research Institute for Microsensor
Systems, Germany

Lecture Title: Design of single flux quantum circuits

Lecture 1: Basic elements and building blocks of SFQ circuit

Lecture 2: Quasistatic circuit analysis of SFQ circuits

Lecture 3: Performance and application of the Josephson
comparator

Lecture 4: Advanced Josephson circuits using phase-shift elements